

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re DIVISIONAL PATENT APPLICATION OF

Inventor(s): MATSUDA et al.

Filed: Herewith

Title: SEMICONDUCTOR DEVICE COMPRISING BURIED CHANNEL  
REGION AND METHOD FOR MANUFACTURING THE SAME

June 24, 2003

**PRELIMINARY AMENDMENT (Revised Format)**

Mail Stop Patent Applications  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Prior to the examination on the merits, please amend this application as follows:

**IN THE SPECIFICATION:**

At the top of the first page, under the title and just under CROSS-REFERENCE TO  
RELATED APPLICATIONS, please insert:

This application is based upon and claims the benefit of priority is a Divisional of  
U.S. Application No. 10/101,950 filed March 21, 2002, which claims priority from the prior  
Japanese Patent Application No. 2001-095899, filed March 29, 2001, the entire contents of  
both of which are incorporated herein by reference.